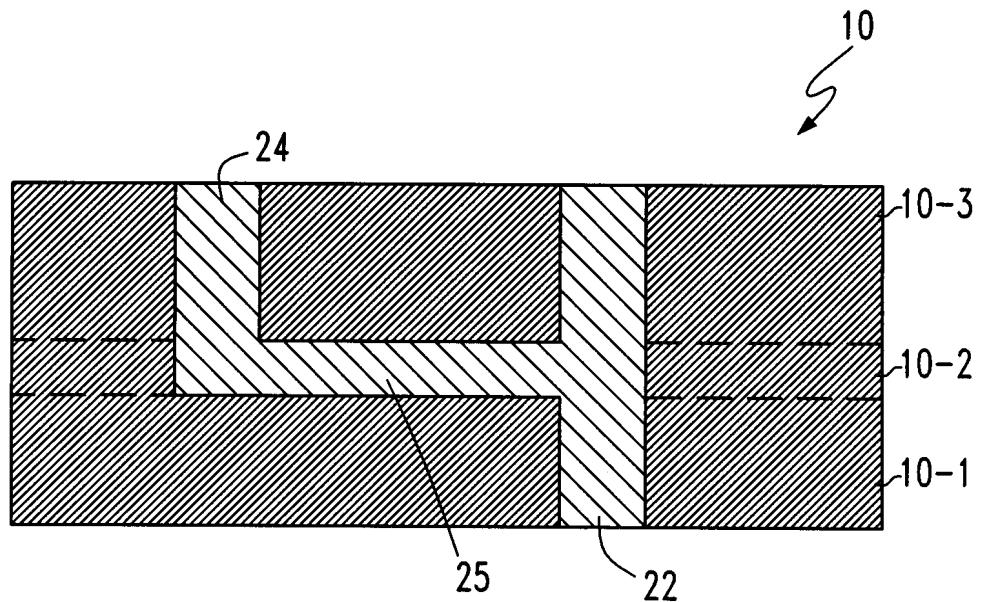
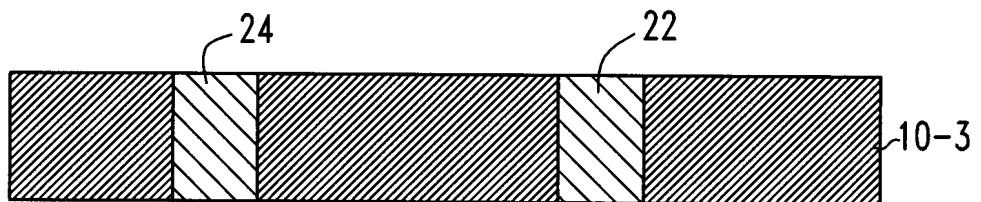


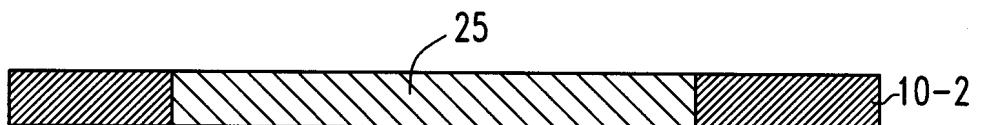
**FIG. 1**



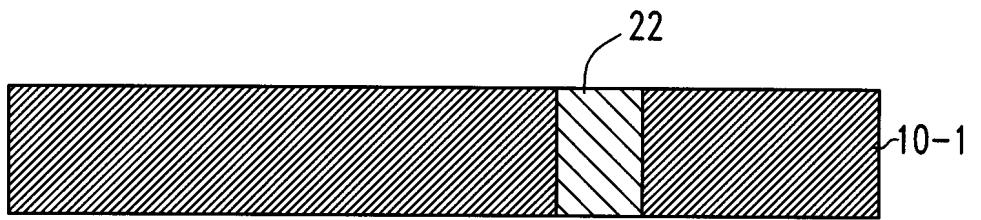
**FIG. 2A**



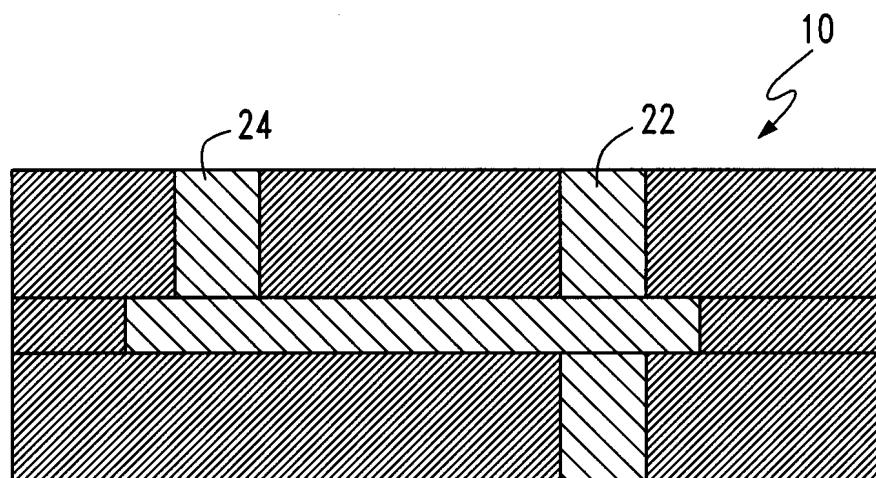
**FIG. 2B**



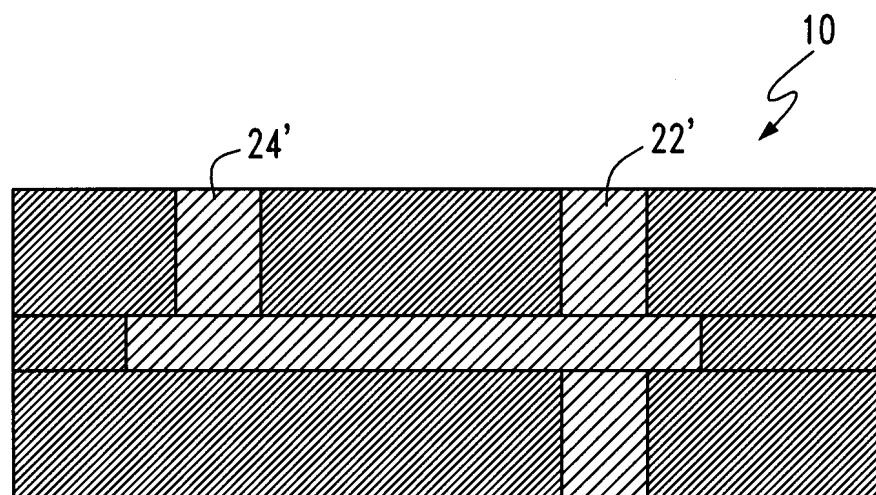
**FIG. 2C**



**FIG. 3**



**FIG. 4**



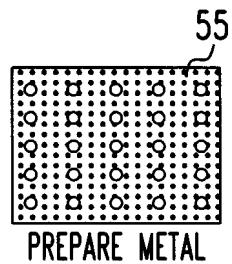
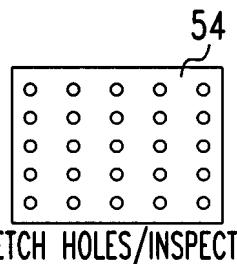
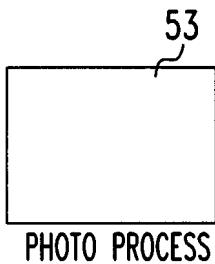
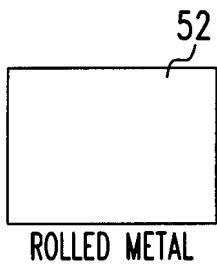


FIG. 5A

FIG. 5B

FIG. 5C

FIG. 5D

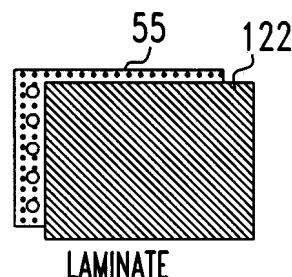
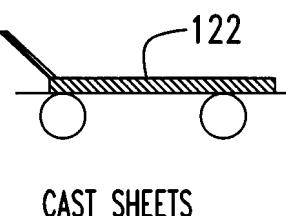
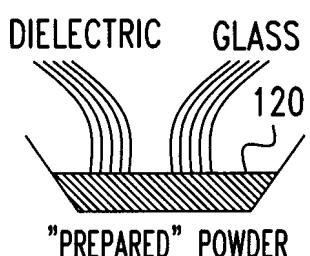


FIG. 5E

FIG. 5F

FIG. 5G

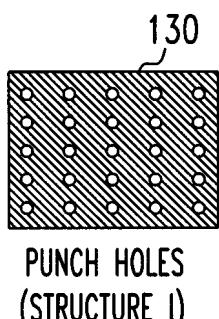
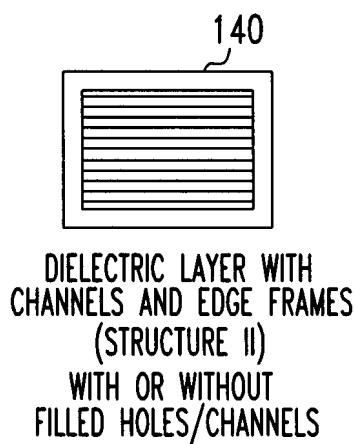


FIG. 5H



DIELECTRIC LAYER WITH CHANNELS AND EDGE FRAMES (STRUCTURE II)  
WITH OR WITHOUT FILLED HOLES/CHANNELS

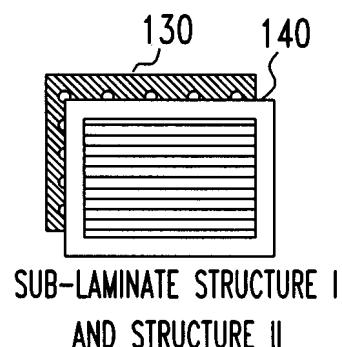
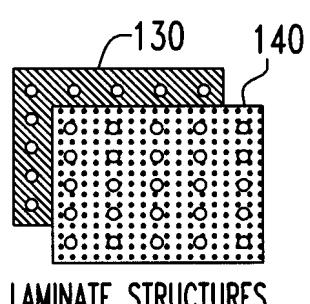


FIG. 5J



LAMINATE STRUCTURES

FIG. 5K



FIG. 5L